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Bulletin Date: 5/17/2016	Bulletin Effective Date: 5/17/2016
Title: EFM8SB2 Errata 0.4	
Bulletin Details	
Description: Silicon Labs is pleased to announce that the following errata is updated and available at www.silabs.com : EFM8SB2 Revision 0.4 Errata	
Reason: EFM8SB2 errata which includes: <ol style="list-style-type: none"> 1.) Addition of a built in firmware workaround for a slowly decaying VDD ramp. This firmware addition is added into revision A devices with date codes 1532 and later. 2.) Addition of Section 2.5 RST_E102 - VDD Monitor Disabled in the errata to explain why a slowly decaying VDD ramp can cause oscillations on the RST pin and VDD monitor to become disabled. A workaround is suggested by having firmware enable the VDD monitor as the first instruction executed after a reset or in startup routine. Please see EFM8SB2 Revision 0.4 Errata.	
Product Identification: EFM8SB20F16G-A-QFN24 EFM8SB20F16G-A-QFN24R EFM8SB20F32G-A-QFN24 EFM8SB20F32G-A-QFN24R EFM8SB20F32G-A-QFN32 EFM8SB20F32G-A-QFN32R EFM8SB20F32G-A-QFP32 EFM8SB20F32G-A-QFP32R EFM8SB20F64G-A-QFN24 EFM8SB20F64G-A-QFN24R EFM8SB20F64G-A-QFN32 EFM8SB20F64G-A-QFN32R EFM8SB20F64G-A-QFP32 EFM8SB20F64G-A-QFP32R	
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Customer Actions Needed: Review above errata.	